

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listing, of claims in the application:

Listing of Claims

1.-8. (Canceled without Prejudice)

9. (Previously Presented) A method of translating a voltage level of a single-ended input signal using a single ended input circuit having at least one native NMOS transistor device having a threshold voltage less than 0V and a gate that is constantly grounded, said method comprising:

outputting a first voltage level if the single ended input signal is in a first state; and

outputting a second voltage level if the single ended input is in a second state.

10. (Original) The method of Claim 9, wherein said first state comprises a high state.

11. (Original) The method of Claim 9, wherein said second state comprises a low state.

12. (Original) The method of Claim 9, wherein said first voltage level comprises a high signal.

13. (Original) The method of Claim 9, wherein said second voltage level comprises a low signal.

14. (Original) The method of Claim 9, wherein said first voltage level comprises a low signal.

15. (Original) The method of Claim 9, wherein said second voltage level comprises a high signal.

16. (Previously Presented) A method of translating a voltage level of a single-ended input signal using a single ended input circuit having at least one native NMOS transistor device having a threshold voltage less than 0V and a gate that is constantly grounded, said method comprising:

determining if the input signal is high;

outputting a low signal if the input signal is high; and

outputting a high signal if the input signal is not high.

17. (Original) The method of Claim 16, wherein determining if the input signal is high comprises determining if the input signal is greater than a first voltage.

18. (Original) The method of Claim 16, wherein determining if the input signal is not high comprises determining if the input signal is less than a second voltage.

19. (Previously Presented) The method of Claim 16, further comprising eliminating static current drain of the level shifter circuit.

20-26. (Cancelled)